



**Series
TF252-80**

**Fast Stud Mounted Thyristor
Type TF252-80**

Low on-state and switching losses
Low reverse recovery charge
Center amplifying gate

Maximum mean on-state current	I _{TAV}	80 A	
Maximum repetitive peak off-state and reverse voltage	U _{DRM}	600 ÷ 1400 V	
Turn-off time	t _q	16, 20; 25 µs	
U _{DRM} , U _{RRM} , V	600	700 800 900 1000 1100 1200 1300 1400	
Voltage code	6	7 8 9 10 11 12 13 14	
Tvj, °C	- 60 ÷ 125		

MAXIMUM ALLOWABLE RATINGS						
Symbols and parameters		Units	TF252-80	Conditions		
ITAV	Mean on-state current	A	80	Tc=90 °C, 180° half-sine wave, 50 Hz		
ITRMS	RMS on-state current	A	126	Tc=90 °C		
ITSM	Surge on-state current	kA	1,6 1,8	Tvj=125°C Tvj=25°C	tp=10 ms UR=0	
I ² t	Limiting load integral	kA ² s	12,8 16,2	Tvj=125°C Tvj=25°C		
UDRM, URRM	Repetitive peak off-state and reverse voltage	V	600÷1400	Tj min≤Tvj≤Tjm 180° half-sine wave, 50 Hz Gate open		
UDSM, URSM	Non-repetitive peak off-state and reverse voltage	V	660÷1500	Tj min≤Tvj≤Tjm 180° half-sine wave tp=10 ms, Single pulse Gate open		
(dit/dt) crit	Critical rate of rise of on-state current : non - repetitive repetitive	A/µs	800 400	Tvj=125°C ; UD=0,67 UDRM, Gate pulse : 10V, 5 Ω, 1µs rise time, 10 µs		
URGM	Peak reverse gate voltage	V	5	Tj min≤Tvj≤Tjm		
Tstg	Storage temperature	°C	-60÷80			
Tvj	Junction temperature	°C	-60÷125			

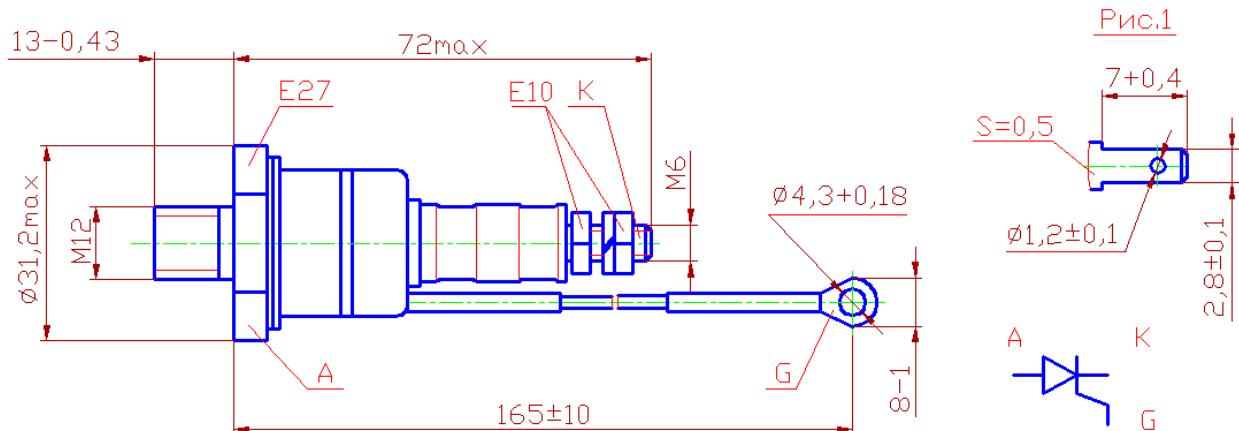
CHARACTERISTICS

UTM	Peak on-state voltage	V	2,2	Tvj=25°C, ITM=3,14 ITAV
UT(TO)	Threshold voltage	V	1,45	Tvj=125°C
R _T	On-state slope resistance	mΩ	3,0	1,57 ITAV < IT < 4,71 ITAV
IDRM IRRM	Repetitive peak off-state and reverse current	mA	20 20	Tvj=125°C, UD = UDRM UR= URRM

CHARACTERISTICS					
Symbols and parameters		Units	TF252-80	Conditions	
I _L	Latching current	A	0,7	Tvj=25°C, UD=12V Gate pulse : 10V, 5Ω, 1 μs rise time, 10μs	
I _H	Holding current	A	0,3	Tvj=25°C, UD=12V, Gate open	
UGT	Gate trigger direct voltage	V	2,5 5,0	Tvj=25°C, Tvj=-60°C	UD=12V
IGT	Gate trigger direct current	A	0,2 0,5	Tvj=25°C, Tvj=-60°C	
UGD	Gate non-trigger direct voltage	V	0,25	Tvj=125°C, UD = 0,67 U _{DRM}	
IGD	Gate non-trigger direct current	mA	10	Direct gate current	
t _{gd}	Delay time	μs	1,6	Tvj=25°C, UD=500V ITM = 80 A	
t _{gt}	Turn-on time	μs	3,2	Gate pulse : 10V, 5Ω, 1 μs rise time, 10μs	
t _q	Turn-off time	μs	16; 20; 25 20; 25; 32	Tvj=125°C, ITM = 80 A di _R /dt = 10 A/μs, UR=100V UD = 0,67 U _{DRM} du _D /dt=50 V/μs du _D /dt=200 V/μs	
Qrr	Recovered charge	μC	100	Tvj=125°C, ITM = 80 A dir/dt = 50 A/μs, UR=100V	
trr	Reverse recovery time	μs	2,5		
Irrm	Peak reverse recovery current	A	80		
(dud/dt)crit	Critical rate of rise of off-state voltage	V/μs	500 1000	Tvj=125°C, UD = 0,67 U _{DRM} Gate open	
R _{thjc}	Thermal resistance junction to case	°C/W	0,21	Direct current	

ORDERING							
	TF	252	80	14	6	5	2
	1	2	3	4	5	6	7

1. Fast thyristor.
2. Design version.
3. Mean on-state current, A.
4. Voltage code (14=1400 V).
5. Critical rate of rise of off-state voltage ($6 \geq 500 \text{ V}/\mu\text{s}$, $7 \geq 1000 \text{ V}/\mu\text{s}$).
6. Group of turn-off time ($\text{du}_D/\text{dt}=50 \text{ V}/\mu\text{s}$, $5 \leq 25 \mu\text{s}$, $6 \leq 20 \mu\text{s}$, $7 \leq 16 \mu\text{s}$).
7. Group of turn-on time ($2 \leq 3,2 \mu\text{s}$).



Tightening torque : 12 ÷ 18 Nm
Weight : 130 grams